

Appln. No. 10/796,615
Docket No. 14XZ120596/GEM-0147

REMARKS / ARGUMENTS

Status of Claims

Claims 1-37 are pending in the application and stand rejected. Applicant has amended Claims 1 and 36, leaving Claims 1-37 for consideration upon entry of the present Amendment.

Applicant respectfully submits that the rejections under 35 U.S.C. §102(b) and 35 U.S.C. §103(a) have been traversed, that no new matter has been entered, and that the application is in condition for allowance.

These amendments and accompanying remarks were not presented earlier because Applicant did not fully appreciate the nature of the Examiner's position until the Applicant was advised in more detail of the position by the final rejection. The claim amendments presented herein, which Applicant respectfully requests entry thereof, should require only a cursory review by the Examiner as they merely include clarifying language.

Claim Objections

The Examiner remarks that Applicant's amended claim language inferentially claims more than one switch, but also refers to "a switch", thereby introducing questionable antecedent basis for the more than one switch.

Applicant has amended Claim 1 and 36 to overcome this objection.

In view of the amendments set forth above, Applicant requests reconsideration and withdrawal of this objection, which Applicant considers to be overcome.

Rejections Under 35 U.S.C. §102(b)

Claims 1-34, 36 and 37 stand rejected under 35 U.S.C. §102(b) as being anticipated by Midya (U.S. Patent No. 6,348,781, hereinafter Midya).

Claims 2-4, 6-8, 15 and 34 stand rejected by Midya per the first Detailed Action.

Applicant traverses this rejection for the following reasons.

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Applicant respectfully submits that “[a] claim is anticipated only if each and every element as set forth in the claim is found, either expressly or inherently described, *in a single prior art reference.*” *Verdegaal Bros. V. Union Oil Co. of California*, 814 F.2d 628, 631, 2 USPQ2d 1051, 1053 (Fed. Cir. 1987) (emphasis added). Moreover, “[t]he identical invention must be shown in as complete detail as is contained in the *** claim.” *Richardson v. Suzuki Motor Co.*, 868 F.2d 1226, 1236, 9 USPQ2d 1913, 1920 (Fed. Cir. 1989). Furthermore, the single source must disclose all of the claimed elements “arranged as in the claim.” *Structural Rubber Prods. Co. v. Park Rubber Co.*, 749 F.2d 707, 716, 223 U.S.P.Q. 1264, 1271 (Fed. Cir. 1984). Missing elements may not be supplied by the knowledge of one skilled in the art or the disclosure of another reference. *Titanium Metals Corp. v. Banner*, 778 F.2d 775, 780, 227 U.S.P.Q. 773, 777 (Fed. Cir. 1985).

Regarding Independent Claim 1

Applicant has amended Claim 1 to now recite, inter alia,

“...a switching cell with *a switch* between the input and the output;
a selector selectively configuring the switching cell into at least two

configurations from among the following:

- a parallel chopper configuration or
- a series chopper configuration or;
- an inductive-storage chopper configuration;

wherein *the switch is configured to chop a voltage of the input in the at least two configurations.*”

No new matter has been added by these amendments as antecedent support may be found in the specification as originally filed, such as at Paragraph [0013] for example.

Dependent claims inherit all of the limitations of the respective parent claim.

Here, Applicant has amended Claim 1 to more clearly describe the claimed invention as having a switch between the input and output that is configured to chop the input voltage regardless of which configuration the converter is in, and to have a selector that is distinct from the switch for configuring the switching cell as desired.

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In alleging anticipation of the claimed invention, the Examiner remarks that Midya comprises switches 30 (buck switch) and 34 (boost switch) wherein the cell is capable of using a single switch (either 30 or 34) in the at least two configurations. Paper 3, pages 2-3.

The Examiner further remarks that Midya fully meets all of the limitations of Claim 1 since Midya Fig. 2 shows switch 30 being used in the closed position to produce a boost circuit, and Midya Fig. 3 shows switch 30 being used in a switching duty cycle to produce a buck circuit. The Examiner then concludes that the switch 30 is a single switch capable of being used in the at least two configurations. The Examiner then applies a similar analysis to the Midya switch 34. Paper 3, pages 6-7.

Applicant appreciates the Examiner's detailed analysis, and has amended Claim 1 as set forth above to more clearly describe the claimed invention.

In view of the foregoing amendment, Applicant submits that Midya fails to disclose wherein *the switch is configured to chop a voltage of the input in the at least two configurations*.

As claimed, Applicant submits that Midya switch 30 is not configured to chop a voltage of the input voltage in both the buck and boost configurations, and that Midya switch 34 is not configured to chop a voltage of the input in both the buck and boost configurations.

At column 3, lines 21-27, Applicant finds Midya to disclose "Whenever the output voltage is required to be greater than the input voltage V_{in} , switching signal Sw2 with duty cycle $D2 > 0$ is applied to Boost switch 34 to produce a Boosted output voltage V_2 . Whenever signal Sw2 is switching to produce Boosted voltage output, the Buck switch 30 remains in the closed position connecting V_{in} with V_{sw1} ."

At column 3, lines 29-32, Applicant finds Midya to disclose "In a similar manner, if output voltage V_2 is required to be less than V_{in} , Buck switch signal Sw1 is switched with a duty cycle of $D1$ while Boost switch 34 remains in the open position."

In view of the foregoing, and in comparing Midya with the claimed invention, Applicant finds Midya to disclose a Boost mode *where Boost switch 34 switches* at duty

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cycle D2, *and where Buck switch 30 is closed*. Also, Applicant finds Midya to disclose a Buck mode *where Buck switch 30 switches* at a duty cycle D1, *and where the Boost switch 34 is open*. Thus, Applicant finds neither of the Boost switch 34 nor the Buck switch 30 to chop the input voltage in both the Boost mode and the Buck mode.

Accordingly, Applicant submits that Midya does not disclose all of the claimed elements arranged as in the claim, and absent anticipatory disclosure in Midya of each and every element of the claimed invention arranged as in the claim, Midya cannot be anticipatory.

Regarding Claims 16-21 More Specifically

The Examiner alleges anticipation by commenting that “Both transistors are continuously conducting during the period of time that they are ON during the duty cycle. This is a reasonable assertion because it is obvious that both transistors cannot be continuously ON forever if the circuit is to work as intended.” Paper 3, page 4.

From the foregoing, it appears that the Examiner is saying that the claimed invention cannot have both transistors continuously ON forever when arranged in the parallel chopper configuration.

However, this is precisely what Applicant is claiming. Where Applicant uses the term “continuously conducting” while in the parallel chopper mode, Applicant means “*continuously conducting*” *while in the parallel chopper mode*, and not “conducting during the period of time that they are ON during a duty cycle”, as alleged by the Examiner. Support for the claimed invention is provided in Paragraph [0018] of the application as originally filed.

If one skilled in the art were to look at the Boost mode of Midya, they would find Boost switch 34 to be switching at duty cycle D2, and most definitely not continuously conducting *while in the boost mode*, which is the claimed invention.

Applicant respectfully submits that the Examiner’s paraphrasing of the claimed invention is entirely contrary to the very language of the claimed invention, and is entirely contrary to the supporting specification.

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Accordingly, Applicant submits that Midya does not disclose each and every element of the claimed invention arranged as claimed and therefore cannot be anticipatory.

Regarding Claims 22-27 More Specifically

The Examiner alleges anticipation by commenting that “Both transistors are continuously non-conducting during the period of time that they are OFF during the duty cycle. This is a reasonable assertion because it is obvious that both transistors cannot be continuously OFF forever if the circuit is to work as intended.” Paper 3, page 4.

From the foregoing, it appears that the Examiner is saying that the claimed invention cannot have both transistors continuously OFF forever when arranged in the series chopper configuration.

However, this is precisely what Applicant is claiming. Where Applicant uses the term “continuously non-conducting” while in the series chopper mode, Applicant means *“continuously non-conducting” while in the series chopper mode*, and not “non-conducting during the period of time that they are OFF during a duty cycle”, as alleged by the Examiner. Support for the claimed invention is provided in Paragraph [0018] of the application as originally filed.

If one skilled in the art were to look at the Buck mode of Midya, they would find Buck switch 30 to be switching at duty cycle D1, and most definitely not continuously non-conducting *while in the buck mode*, which is the claimed invention.

Applicant respectfully submits that the Examiner’s paraphrasing of the claimed invention is entirely contrary to the very language of the claimed invention, and is entirely contrary to the supporting specification.

Accordingly, Applicant submits that Midya does not disclose each and every element of the claimed invention arranged as claimed and therefore cannot be anticipatory.

Regarding Claim 37 More Specifically

The Examiner alleges anticipation by commenting that “switch 30 always has a period of continuous conduction” including “during the ON period when switch 30 itself

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is being switched at a duty cycle", and that "switch 34 always has a period of continuous non-conduction" including "during the OFF period when switch 34 itself is being switched at a duty cycle". The Examiner further comments that "Limiting the continuous conduction and non-conduction periods to only the part of the duty cycle when the switches are on and off respectively, is a reasonable assertion because it is obvious that both transistors cannot be continuously ON or continuously OFF forever if the circuit is to work as intended." Paper 3, page 5.

From the foregoing, it appears that the Examiner is saying that the claimed invention cannot have the first transistor continuously ON and the second transistor continuously OFF forever when arranged in an inductive-storage chopper configuration.

However, this is precisely what Applicant is claiming. Where Applicant uses the term "continuously conducting" and "continuously non-conducting" while in the inductive-storage chopper mode, Applicant means "*continuously conducting*" and "*continuously non-conducting*" while in the inductive-storage chopper mode, and not conducting only during an ON duty cycle and non-conducting only during an OFF duty cycle, as alleged by the Examiner. Support for the claimed invention is provided in Paragraph [0018] of the application as originally filed.

Applicant respectfully submits that the Examiner's paraphrasing of the claimed invention is entirely contrary to the very language of the claimed invention, and is entirely contrary to the supporting specification.

Accordingly, Applicant submits that Midya does not disclose each and every element of the claimed invention arranged as claimed and therefore cannot be anticipatory.

Regarding Claim 36 More Specifically

Applicant has amended Claim 36 in a manner similar to amended Claim 1 for similar reasons. In view of the amendment and foregoing remarks, Applicant submits that Claim 36 is allowable at least for the reason that it depends from an allowable claim.

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In view of the amendment and foregoing remarks, Applicant submits that Midya does not disclose each and every element of the claimed invention arranged as claimed and therefore cannot be anticipatory. Accordingly, Applicant respectfully submits that the Examiner's rejection under 35 U.S.C. §102(b) has been traversed, and requests that the Examiner reconsider and withdraw of this rejection.

Rejections Under 35 U.S.C. §103(a)

Claim 35 stands rejected un 35 U.S.C. §103(a) as being unpatentable over Midya in view of Usui (U.S. Patent Publication No. 2002/0011825, hereinafter Usui) per the first Detailed Action.

Applicant traverses this rejection for the following reasons.

Applicant respectfully submits that if an independent claim is non-obvious, then any claim depending therefrom is non-obvious. MPEP §2143.03 referencing *In re Fine*, 5 USPQ2d 1596 (Fed. Cir. 1988).

In view of Claim 35 being dependent upon Claim 1, and in view of the amendment and foregoing remarks, Applicant submits that Claim 35 is allowable at least for the reason that it depends from an allowable claim.

Applicant has amended the claims for presentation in a better form that more clearly reflects Applicant's invention. The claim amendments should only require a cursory review by the Examiner as they merely include clarifying language.

In light of the foregoing remarks and amendments, Applicant respectfully submits that the proposed amendments and arguments comply with 37 C.F.R. §1.116 and should therefore be entered, and with their entry that the Examiner's rejections under 35 U.S.C. §102(b) and 35 U.S.C. §103(a) have been traversed, and that the application is now in condition for allowance. Such action is therefore respectfully requested.

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The Commissioner is hereby authorized to charge any additional fees that may be required for this amendment, or credit any overpayment, to Deposit Account No. 50-2513. In the event that an extension of time is required, or may be required in addition to that requested in a petition for extension of time, the Commissioner is requested to grant a petition for that extension of time that is required to make this response timely and is hereby authorized to charge any fee for such an extension of time or credit any overpayment for an extension of time to the above-identified Deposit Account.

Respectfully submitted,

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